

# **DW9274**

## 92.025MHz IF SAW Filter

Replaces July 2002 version, DS4082-2.1

DS4082-3.0 December 2003

Reference level for the relative attenuation  $a_{\rm rel}$  of the DW9274 is the insertion loss. The insertion loss  $a_{\rm e}$  is defined as the insertion loss at the nominal frequency  $f_{\rm N}$ . The centre frequency  $f_{\rm C}$  is the arithmetic mean value of the upper and lower frequencies at the 3 dB filter attenuation level relative to the insertion loss  $a_{\rm e}$ . The temperature coefficient of frequency  $T_{\rm cl}$  is valid both for the reference frequency  $f_{\rm C}$  and the frequency response of the filter at the operating temperature. The frequency shift of the filter at the operating temperature not included in the production tolerance scheme.

### **FEATURES**

- 92.025MHz Centre Frequency (fo)
- Insertion Loss 3dB (Typ)
- 3dB Bandwidth 30kHz (Min)
- Quartz Temperature Stability

### **ABSOLUTE MAXIMUM RATINGS**

DC Voltage VDV 0V Maximum Input Level 0dBm

#### **ORDERING INFORMATION**

Order as: DW9274

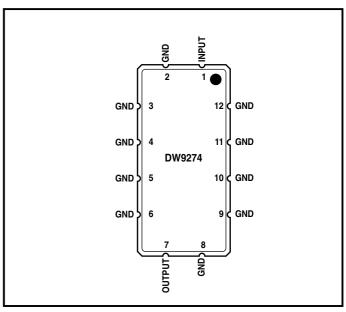


Fig. Pin connections



# **ELECTRICAL SPECIFICATION** @23°C

Parameter	Symbol	Typical Value	Max. Limit/ Toerance	Units
Insertion loss (Reference level)		3.0	4.0 max	dB
Nominal frequency (at ambient temperature)		-	92.025	MHz
Centre frequency (at ambient temperature)	f <sub>c</sub>	92.025	-	MHz
Pass band		-	f <sub>c</sub> ± 0.015	MHz
Pass band ripple		0.5	1.0 max	dB
Bandwidth	BW	-	3	dB
3dB		63	30 min	kHz
Relative attenuation $f_{c} \pm 50 \text{ kHz } f_{c} \pm 100 \text{ kHz}$ $f_{c} + 100 \text{ kHz } f_{c} + 200 \text{ kHz}$ $f_{c} + 200 \text{ kHz } f_{c} + 500 \text{ kHz}$ $f_{c} + 500 \text{ kHz } f_{c} + 1,0 \text{ MHz}$ $f_{c} - 100 \text{ kHz } f_{c} - 300 \text{ kHz}$ $f_{c} - 300 \text{ kHz } f_{c} - 900 \text{ kHz}$ $f_{c} - 900 \text{ kHz } f_{c} - 920 \text{ kHz}$ $f_{c} - 920 \text{ kHz } f_{c} - 1,0 \text{ MHz}$	a <sub>rel</sub>	- - - - - - -	5.0 35.0 25.0 40.0 35.0 60.0 70.0 60.0	dB dB dB dB dB dB dB
Group delay ripple (within PB)		-	4 max	μs
Input power level		-	0	dBm
Operating temperature range		-	+65	°C
Storage temperature range		-	+85	°C
Temperature coefficient	T <sub>cf</sub>	-	-	

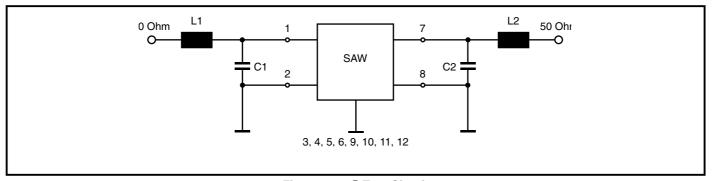


Figure 2: 50 $\Omega$  Test Circuit



### **PACKAGE DETAILS**

Dimensions are shown thus: mm (in). DO NOT SCALE. For further package information, please contact Customer Services.

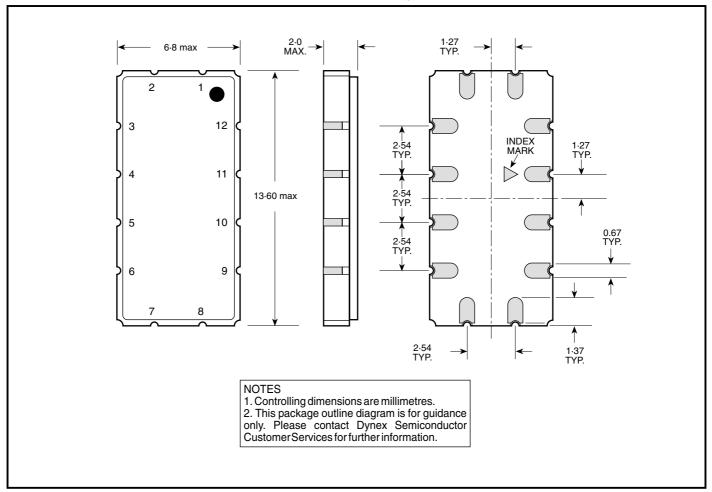


Figure 3 Package outline detials





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- P:- Preliminary information represents the product as it is understood but details may change as the product is in design and development.
- A:- Advance information denotes the product design is complete and final characterisation for volume production is well in hand.
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